

PATENT
(5500-89400/TT5381)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/615,101

Filed: July 8, 2003

Inventor(s):

Michael A. Filippo
James K. Pickett

Title: Store-to-Load Forwarding
Buffer Using Indexed
Lookup

§ Examiner: Unknown
§ Group/Art Unit: 2121
§ Atty. Dkt. No: 5500-89400

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Robert C. Kowert

Name of Registered Representative

 November 17, 2003
Signature Date

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant requests consideration of the references listed on the attached Form PTO-1449 and/or the additional information identified below in paragraph 3. A copy of each reference listed on the Form PTO-1449 is enclosed.

1. This Information Disclosure Statement is submitted:

- a. within 3 months of the filing date of a national application other than a continued prosecution application under § 1.53(d);
 within 3 months of the date of entry of the national stage as set forth in § 1.491 in an International application;
 before the mailing date of a first Office Action on the merits; or
 before the mailing of a first Office Action after the filing of a request for continued examination under § 1.114.

- b. after the events of above paragraph 1a and prior to the mailing date of a final Office Action or Notice of Allowance, and thus: the certification of paragraph 2 below is provided, or a fee of \$180.00 is enclosed.

c. after the mailing date of a final Office Action or a Notice of Allowance and prior to payment of the issue fee, and thus: the certification of paragraph 2 below is provided and a fee of \$180.00 is enclosed.

2. It is hereby certified:

that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Statement, or

that no item of information contained in the Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the person signing the certification after making reasonable inquiry, was known to any individual designated in § 1.56 (c) more than three months prior to the filing of the Statement.

3. Consideration of the following additional information (including any co-pending or abandoned U.S. applications, prior uses and/or sales, etc.) is requested:

4. For each non-English language reference listed on the attached Form PTO-1449:

reference is made to an English language translation submitted herewith, and/or

reference is made to a foreign patent office search report (in the English language) submitted herewith, and/or

reference is made to an English language translation of a foreign patent office search report submitted herewith, and/or

reference is made to the concise explanation contained in the specification of the present application at page(s) _____, and/or

reference is made to the concise explanation set forth below:

5. Applicant also offers the following comments for the Examiner's consideration:

6. Also enclosed is a copy of a foreign search report citing these references.

7. The listed documents were brought to the attention of the Applicant(s) after payment of the issue fee in the captioned case. The documents were cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. Applicant(s) request this Information Disclosure Statement and attached Form PTO-1449 be placed in the file of the captioned application.

8. Applicant(s) requests that the Information Disclosure Statement and attached Form PTO-1449 and references, which are being filed before the grant of the patent and pursuant to 37 C.F.R. § 1.97(i), be placed in the file of the captioned application.

If any required fees are missing, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 50-1505/5500-89400/RCK.

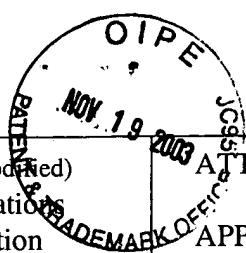
Respectfully submitted,



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Date: November 17, 2003



Form PTO-1449 (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)			ATTY. DKT. NO. 5500-89400 APPLICANT: Filippo, et al. FILING DATE: July 8, 2003	SERIAL NO. 10/615,101 GROUP: 2121			
U.S. PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
FOREIGN PATENT DOCUMENTS							
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
	A1	96/12227	4/25/96	WO			
	A2	00/11548	3/2/00	WO			
	A3	709 770	5/1/96	EP			
	A4	97/27538	7/31/97	WO			
	A5	2 281 422	3/1/95	GB			
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
	A6	Moshovos, et al., "Dynamic Speculation and Synchronization of Data Dependences," ACM, Published June 2, 1997, XP-000738156, pp. 181-193.					
	A7	Chrysos, et al., "Memory Dependence Prediction Using Store Sets," IEEE, Published June 27, 1998, XP-000849912, pp. 142-153.					
	A8	Leibholz, et al., "The Alpha 21264: A 500 MHz Out-of-Order Execution Microprocessor," IEEE, published February 23, 1997, XP-000751757, pp. 28-36.					
	A9	Popescu, et al., "The Metaflow Architecture," IEEE Micro, June 1991, pp. 10-13 and 63-73.					
	A10	Gwennap, "Digital 21264 Sets New Standard," Microprocessor Forum, Vol. 10, NO. 14, Microdesign Resources, October 28, 1996, pp. 103-108.					
	A11	Mike Johnson, "Superscalar Microprocessor Design," Advanced Micro Devices, Prentice Hall 1991, pp. 130-132, 147-163.					
	A12	Moshovos, et al., "Streamlining Inter-operation Memory Communication via Data Dependence Prediction," IEEE Proceedings of Micro-30, December 1997, 11 pages.					
	A13	Moshovos, et al., "Speculative Memory Cloaking and Bypassing," October 1999, 15 pages.					
	A14	Gopal, et al., "Speculative Versioning Cache," February 1998, 11 pages.					

EXAMINER:

DATE CONSIDERED:

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.